

DIAGRAMA UNIFILAR CERAL

The diagram shows three stages of a unifilar ceramic circuit, labeled QD1, QD2, and QD3. Each stage consists of a 100k resistor and a 100pF capacitor. The stages are connected in series, with the output of one stage feeding into the input of the next. The diagram includes a 100k resistor, a 100pF capacitor, and a 100k resistor. The diagram is labeled with component values and pin numbers.

DETALHE CX.DIST.(60X60)
(ESQUEMA DE LIGAÇÃO)

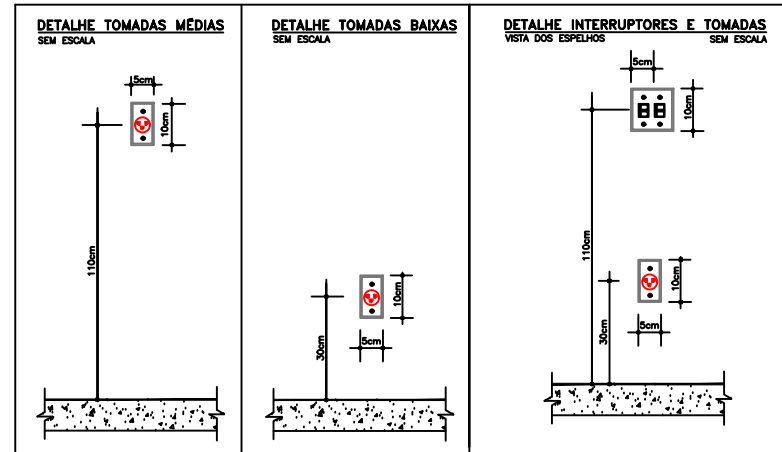
Vem do a50

P/001 P/002 P/003 P/004

A B C D N T

1/4 fibra

MADE 2,4 WTS.

[illegible]

TIPO DE USO :

CMEI INÊS DIAS DA SILVA

ENDEREÇO DA OBRA :
RUA 308 Nº 395
JARDIM CATALÃO

PROPRIETÁRIO

PREFEITURA MUNICIPAL DE CATALÃO

Nº DA PRANCHA :

RESPONSÁVEL TÉCNICO:

CREA n° 7456/D

VER NO ARQUITETÔNICO